WHAT IS CLAIMED IS:

1. A method for performing a write operation in a non-volatile memory system, the non-volatile memory system including a non-volatile memory having a memory unit, the memory unit being associated with a logical unit, the memory unit including a first physical sub-unit and a second physical sub-unit, the second physical sub-unit being sequentially after the memory unit than the first physical sub-unit, the logical unit including a first logical sub-unit and a second logical sub-unit, the second logical sub-unit being sequentially after the first logical sub-unit, the method comprising:

writing a first set of contents into the first physical sub-unit; and

writing a second set of contents into the second physical sub-unit after the first set of contents is written into the first physical sub-unit, wherein the first set of contents is associated with the second logical sub-unit and the second set of contents is associated with the first logical sub-unit.

2. The method of claim 1 wherein the first physical sub-unit is a part of a first physical group and the second physical sub-unit is a part of a second physical group, the first physical group and the second physical group being associated with the memory unit.

3. The method of claim 2 wherein the second physical group includes a plurality of sub-units, the plurality of sub-units including the second physical sub-unit, and wherein after the second set of contents is written into the second physical sub-unit, substantially any contents written into the plurality of sub-units included in the second physical group are associated with logical sub-units which are sequentially after the first logical sub-unit.

4. The method of claim 2 further including:

writing a first identifier into the first physical group, the first identifier being arranged to substantially identify the second logical sub-unit; and writing a second identifier into the second physical group, the second identifier being arranged to substantially identify the first logical sub-unit.

5. The method of claim 1 wherein the memory unit is a physical block, the logical unit is a logical block, the first physical sub-unit is a first physical page, the second physical sub-unit is a second physical page, the first logical sub-unit is a first logical page, and the second logical sub-unit is a second logical page.

6. The method of claim 5 wherein the non-volatile memory is a flash memory.

7. The method of claim 6 wherein the flash memory is one of a NAND flash memory and an MLC NAND flash memory.

8. A method for performing a write operation in a flash memory system, the flash memory system including a memory unit, the memory unit being associated with a logical unit, the memory unit including a first physical sub-unit and a second physical sub-unit, the second physical sub-unit being sequentially after the memory unit than the first physical sub-unit, the logical unit including a first logical sub-unit, the method comprising:

writing a first set of contents into the first physical sub-unit; and

writing a second set of contents into the second physical sub-unit after the first set of contents is written into the first physical sub-unit, wherein the first set of contents and the second set of contents are associated with the first logical sub-unit, the second set of contents being arranged to substantially supercede the first set of contents.

9. The method of claim 8 wherein the first physical sub-unit is a part of a first physical group and the second physical sub-unit is a part of a second physical group, the first physical group and the second physical group being associated with the memory unit.

10. The method of claim 9 wherein the first logical sub-unit is included in a first logical grouping within the logical unit, the method further including:

writing a first indicator into the first physical group that substantially identifies the first logical grouping as being associated with the first physical group; and

writing a second indicator into the second physical group that substantially identifies the first logical grouping as being associated with the second physical group.

11. The method of claim 9 wherein the second physical group includes a plurality of sub-units, the plurality of sub-units including the second physical sub-unit, and wherein after the second set of contents is written into the second physical sub-unit, substantially any contents written into the plurality of sub-units included in the second physical group are associated with logical sub-units which are sequentially after the first logical sub-unit.

12. The method of claim 8 wherein the memory unit is a physical block, the logical unit is a logical block, the first physical sub-unit is a first physical page, the second physical sub-unit is a second physical page, and the first logical sub-unit is a first logical page.

13. The method of claim 12 wherein the non-volatile memory is a flash memory.

14. The method of claim 13 wherein the flash memory is one of a NAND flash memory and an MLC NAND flash memory.

15. A non-volatile memory system comprising:

a non-volatile memory having a memory unit, the memory unit being associated with a logical unit, the memory unit including a first physical sub-unit and a second physical sub-unit, the second physical sub-unit being sequentially after the memory unit than the first physical sub-unit, the logical unit including a first logical sub-unit and a second logical sub-unit, the second logical sub-unit being sequentially after the first logical sub-unit;

code devices that cause a first set of contents to be written into the first physical sub-unit;

code devices that cause a second set of contents to be written into the second physical sub-unit after the first set of contents is written into the first physical sub-unit, wherein the first set of contents is associated with the second logical sub-unit and the second set of contents is associated with one of the first logical sub-unit and the second logical sub-unit; and

a memory portion that stores the code devices.

16. The non-volatile memory system of claim 15 wherein when the second set of contents is associated with the second logical sub-unit, the second set of contents is arranged to substantially supercede the first set of contents.

17. The non-volatile memory system of claim 15 wherein the first physical sub-unit is a part of a first physical group and the second physical sub-unit is a part of a second physical group, the first physical group and the second physical group being associated with the memory unit.

18. The non-volatile memory system of claim 17 further including code devices that cause additional contents to be written into the second physical group, the second physical group including a plurality of sub-units, the plurality of sub-units including the second physical sub-unit, wherein after the second set of contents is written into the second physical sub-unit, substantially any contents written into the plurality of sub-units included in the second physical group are associated with logical sub-units which are sequentially after the first logical sub-unit.

19. The non-volatile memory system of claim 15 wherein the memory unit is a physical block, the logical unit is a logical block, the first physical sub-unit is a first physical page, the second physical sub-unit is a second physical page, the first logical sub-unit is a first logical page, and the second logical sub-unit is a second logical page.

20. The non-volatile memory system of claim 19 wherein the non-volatile memory is a flash memory.

21. The non-volatile memory system of claim 20 wherein the flash memory is one of a NAND flash memory and an MLC NAND flash memory.

22. A non-volatile memory system comprising:

a non-volatile memory having a memory unit, the memory unit being associated with a logical unit, the memory unit including a first physical sub-unit and a second physical sub-unit, the second physical sub-unit being sequentially after the memory unit than the first physical sub-unit, the logical unit including a first logical sub-unit and a second logical sub-unit, the second logical sub-unit being sequentially after the first logical sub-unit;

means for causing a first set of contents to be written into the first physical sub-unit; and

means for causing a second set of contents to be written into the second physical sub-unit after the first set of contents is written into the first physical sub-unit, wherein the first set of contents is associated with the second logical sub-unit and the second set of contents is associated with one of the first logical sub-unit and the second logical sub-unit.

23. A physical memory unit, the physical memory unit being located within a non-volatile memory, the physical memory unit being associated with a logical memory unit having a first logical grouping and a second logical grouping, the second logical grouping being sequentially after the first logical grouping within the logical memory unit, the physical memory unit comprising:

a first physical grouping;

a second physical grouping;

a first physical sub-unit, the first physical sub-unit being included in the first physical grouping, the first physical sub-unit including a first set of contents, the first set of contents being associated with the second logical grouping, wherein a first identifier which substantially identifies the first set of contents as being associated with the second logical grouping is stored in the first physical grouping; and

a second physical sub-unit, the second physical sub-unit being included in the second physical grouping, the second physical sub-unit including a second set of contents, the second set of contents being associated with the first logical grouping, wherein a second identifier which substantially identifies the second set of contents as being associated with the first logical grouping is stored in the second physical grouping.

24. The physical memory unit of claim 23 wherein the physical memory unit is a physical block, the logical memory unit is a logical block, the first physical sub-unit is a first physical page, and the second physical sub-unit is a second physical page.

25. The physical memory unit of claim 23 wherein the first physical grouping includes a first plurality of physical sub-units and the second physical grouping includes a second plurality of physical subunits, and wherein the first identifier is stored in a first physical sub-unit of the first plurality of physical sub-units located at the beginning of the first physical grouping and the second identifier is stored in a second physical sub-unit of the second plurality of physical sub-units located at the beginning of the beginning of the second physical grouping.

26. The physical memory unit of claim 23 wherein the non-volatile memory is a flash memory.

27. The physical memory unit of claim 26 wherein the flash memory is one of a NAND flash memory and an MLC NAND flash memory.

28. A method for providing contents associated with a logical block to a physical block, the physical block being included in a non-volatile memory of a memory system, the method comprising:

obtaining a first set of contents associated with a first logical page of a first logical group of the logical block; and

storing the first set of contents in the physical block, wherein storing the first set of contents in the physical block includes storing the first set of contents in a first physical page of a first physical group of the physical block and storing an indicator in the first physical group which identifies the first set of contents as being associated with the first logical group.

29. The method of claim 28 wherein the first physical group includes a plurality of physical pages, the plurality of physical pages including a beginning physical page, the beginning physical page being arranged at the beginning of the first physical group, and wherein storing the indicator in the first physical group includes storing the indicator in the beginning physical page.

30. The method of claim 28 further including:

obtaining a second set of contents associated with the logical block; and

storing the second set of contents in the first physical group when the second set of contents is associated with a second logical page of the first logical group and the second logical page of the first logical group is sequentially after the first logical page within the first logical group.

31. The method of claim 28 further including:

obtaining a second set of contents associated with the logical block;

storing the second set of contents in a second physical group of the physical block when the second set of contents is associated with the first logical page of the first logical group; and storing an identifier in the second physical group which identifies the second set of contents as being associated with the first logical group.

32. The method of claim 28 further including:

obtaining a second set of contents associated with the logical block;

storing the second set of contents in a second physical group when the second set of contents is associated with a second logical page of a second logical group of the logical block; and

storing an indicator in the second physical group which identifies the second set of contents as being associated with the second logical group.

33. The method of claim 32 wherein the first logical page is sequentially after the second logical page in the logical block and the second physical group is sequentially after the first physical group in the physical block.

34. The method of claim 28 wherein the non-volatile memory is a flash memory.

35. The method of claim 34 wherein the flash memory is one of a NAND flash memory and an MLC NAND flash memory.

36. A memory system comprising:

code devices arranged to a first set of contents associated with a first logical page of a first logical group of a logical block;

code devices arranged to store the first set of contents in a physical block of a non-volatile memory, wherein the code devices arranged to store the first set of contents in the physical block include code devices arranged to store the first set of contents in a first physical page of a first physical group of the physical block and to store an indicator in the first physical group which identifies the first set of contents as being associated with the first logical group; and

a storage for storing the code devices.

37. The memory system of claim 36 wherein the first physical group includes a plurality of physical pages, the plurality of physical pages including a beginning physical page, the beginning physical page being arranged at the beginning of the first physical group, and wherein the code devices arranged to store the indicator in the first physical group include code devices arranged to store the indicator in the first physical page.

38. The memory system of claim 36 further including:

code devices arranged to obtain a second set of contents associated with the logical block; and

code devices arranged to store the second set of contents in the first physical group when the second set of contents is associated with a second logical page of the first logical group and the second logical page of the first logical group is sequentially after the first logical page within the first logical group.

39. The memory system of claim 36 further including:

code devices arranged to obtain a second set of contents associated with the logical block;

code devices arranged to store the second set of contents in a second physical group of the physical block when the second set of contents is associated with the first logical page of the first logical group; and

code devices arranged to store an identifier in the second physical group which identifies the second set of contents as being associated with the first logical group.

40. The memory system of claim 36 further including:

code devices arranged to obtain a second set of contents associated with the logical block;

code devices arranged to store the second set of contents in a second physical group when the second set of contents is associated with a second logical page of a second logical group of the logical block; and code devices arranged to store an indicator in the second physical group which identifies the second set of contents as being associated with the second logical group.

41. The memory system of claim 40 wherein the first logical page is sequentially after the second logical page in the logical block and the second physical group is sequentially after the first physical group in the physical block.

42. The memory system of claim 36 wherein the code devices are one of software code devices and firmware code devices.